

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Akio Iwabuchi  
Serial Number: 10/566,421  
Filed: January 30, 2006  
Group Art Unit: 2815  
Confirmation No.: 3632  
Examiner: Anthony Ho  
Title: SEMICONDUCTOR DEVICE WITH IMPROVED  
ELECTROSTATIC TOLERANCE

Attorney Docket No.: 060233.00025

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**"MAKE OF RECORD" LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The following errors have been found in the above-identified United States Letters

Patent:

**Drawing Sheets**

On drawing sheet 3 of 6, please delete **4b**.

On drawing sheet 3 of 6, please add **14b**.

It is respectfully submitted that the foregoing corrections be made of record in the file of the subject patent.

Although it is believed that no fee is due for the filing of this letter, the Commissioner is authorized to charge our Deposit Account No. 08-2789 for any additional fees or credit the account for any overpayments regarding this submission.

Respectfully submitted,

**HOWARD & HOWARD ATTORNEYS PLLC**

Dated: December 8, 2009

**/Gregory D. DeGrazia/**

**Gregory D. DeGrazia, Reg. No. 48,944**

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